WEST

Freeform Search

Database:

US Patents Full-Text Database
US Pre-Grant Publication Full-Text Database
JPO Abstracts Database
EPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Term:

Display: [10] Documents in **Display Format:** [TI] **Starting with Number** [1]

Generate: ○ **Hit List** ◉ **Hit Count** ○ **Side by Side** ○ **Image**

Search	Clear	Help	Logout	Interrupt

Main Menu	Show S Numbers	Edit S Numbers	Preferences	Cases

Search History

DATE: **Thursday, July 31, 2003**   Printable Copy   Create Case

Set Name side by side	Query	Hit Count	Set Name result set
	DB=USPT; PLUR=YES; OP=ADJ		
L7	L6 and (interface near8 bus)	69	L7
L6	L5 and (bus near8 (clock or rate or timing or factor or ratio))	104	L6
L5	L4 and l3	157	L5
L4	L1 same (bus near4 (width or bandwidth or size or wide or capacity))	371	L4
L3	L2 same (bus near4 (width or bandwidth or size or wide or capacity))	5002	L3
L2	(memory or external) near8 bus	63758	L2
L1	(module or card or SIMM or DIMM) near8 (data near4 bus)	5959	L1

END OF SEARCH HISTORY

WEST

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Search History

DATE: Thursday, July 31, 2003 Printable Copy Create Case

Set Name Query	Hit Count	Set Name
side by side		result set

DB=USPT; PLUR=YES; OP=ADJ

Set Name	Query	Hit Count	Set Name
L5	L4 and (interface with bus)	48	L5
L4	L3 and (memory near4 control$4)	73	L4
L3	L2 same L1 same (bus near4 (clock or speed or frequency or rate or bandwidth or width or wide))	79	L3
L2	memory near4 data near4 bus	20383	L2
L1	module near4 data near4 bus	2800	L1

END OF SEARCH HISTORY